

-	0	"5862150" and signature and compar\$4 and register\$1 and (test\$4 or debug\$4 or error or analysis or analyz\$4)and display\$3 near4 (channel\$1 or stream\$1 or field\$1 or areas)	USPAT	2004/05/26 19:35
-	0	"5862150" and signature and compar\$4 and register\$1 and (test\$4 or debug\$4 or error or analysis or analyz\$4)and display\$3 near4 (channel\$1 or stream\$1 or field\$1 or areas or region\$1)	USPAT	2004/05/26 19:35
-	2	"5862150" and signature and compar\$4 and register\$1 and (test\$4 or debug\$4 or error or analysis or analyz\$4)and display\$3	USPAT	2004/05/26 19:35
-	10	"5754170"	USPAT	2004/05/26 20:31
-	11	"5767866"	USPAT	2004/05/26 20:31
-	19	"5754170" or "5767866"	USPAT	2004/05/26 20:32
-	2	"5754170" and "5767866"	USPAT	2004/05/26 20:31
-	10	("5754170" or "5767866") and compar\$4	USPAT	2004/05/26 20:32
-	8	"5754170" or "5767866") and compar\$4 same(register\$1 or arbiter\$1	USPAT	2004/05/26 20:32
-	8	("5754170" or "5767866") and compar\$4 same(register\$1 or arbiter\$1) and display\$4	USPAT	2004/05/26 20:33
-	3	("5754170" or "5767866") and compar\$4 same(register\$1 or arbiter\$1) and display\$4 near4 (channel\$1 or stream\$1 or field\$1)	USPAT	2004/05/26 20:33
-	611	oscilloscope and web	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:25
-	30	oscilloscope and web near3 processing	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:06
-	15	digital\$2 same oscilloscope and web near3 processing	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:10
-	2	digital\$2 same oscilloscope and web near3(manag\$4 and processing)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:18
-	16	digital\$2 same oscilloscope and web near3(manag\$3 or processing)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:11
-	4	digital\$2 same oscilloscope and web near3(manag\$3 or processing)and 702/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:14
-	2	digital\$2 same oscilloscope same web near3(manag\$3 or processing)and 702/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:11
-	29	"4634970"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:15
-	27	"4818932"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:16

-	9	"4906916"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:16
-	2	digital\$2 same oscilloscope and web near3(manag\$4 and processing) and web same element	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:19
-	2	digital\$2 same oscilloscope and web near3(manag\$4 and processing) and web same element and web same (input\$1 and output\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:19
-	2	digital\$2 same oscilloscope and web near3(manag\$4 and processing) and web same element and web same (input\$1 and output\$1) and web same pin	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:20
-	0	digital\$2 same oscilloscope same web near3(manag\$4 and processing) and web same element and web same (input\$1 and output\$1) and web same pin	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:20
-	2	digital\$2 same oscilloscope and web near3(manag\$4 and processing) and web same element and web same (input\$1 and output\$1) same web same pin	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:21
-	2	digital\$2 same oscilloscope and web near3(manag\$4 and processing) and web same element and web same (input\$1 and output\$1) same web same pin and oscilloscope\$1 same (web and element\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:22
-	209	oscilloscope and web and network\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:26
-	87	oscilloscope and (web same network\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:27
-	69	oscilloscope and (web same network\$1) and (web or network\$1) same (process\$3 or edit\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:28
-	5	oscilloscope same ( (web same network\$1) and (web or network\$1) same (process\$3 or edit\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:30
-	68	oscilloscope and ( (web same network\$1) and (web or network\$1) same (process\$3 or edit\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:31
-	50	oscilloscope and ( (web near15 network\$1) and (web or network\$1) same (process\$3 or edit\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:31

-	31	oscilloscope and ( (web near15 network\$1) and (web same network\$1) same (process\$3 or edit\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:31
-	4	oscilloscope same (web near15 network\$1) and (web same network\$1) near15 (process\$3 or edit\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 00:32
-	20	oscilloscope and ( (web near15 network\$1) and (web same network\$1) near15 (process\$3 or edit\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/28 14:10
-	3	"6480856"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/28 14:10
-	2	"6480856" and shape	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/28 16:13
-	3	"6480856" and (expression or program\$4 or object )	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/28 16:19
-	1	"6480856" and declarative	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/28 16:20

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"4906916"

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6539318 B2	20030325	13	Streaming architecture for waveform processing	702/66	324/121R
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5255370 A	19931019		Data transfer apparatus and data transfer system	710/52	710/316
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5235270 A	19930810		Waveform measuring device	324/121R	702/67
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5235269 A	19930810		Waveform measuring device	324/121R	702/67
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5233537 A	19930803		Very low frequency and low frequency transmitting antenna parameter	702/65	343/703
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5223784 A	19930629		Method and circuit for triggering an electronic instrument only once during a	324/121R	324/73.1
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5153501 A	19921006		Waveform measuring device	324/121R	702/67
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4906916 A	19900306	17	Signal processing device having high speed shift register	324/121R	324/115
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	DE 3511592 A	19861002		Signal processor esp. digital storage oscillooraph - contains digital high speed		

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1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6731291 B2	20040504	54	Image processing device and system using the same	345/503	345/610
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6714475 B2	20040330		Fast accessing of a memory device using decoded address during setup	365/230.01	365/230.06; 365/236
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6587918 B1	20030701		Method for controlling refresh of a multibank memory device	711/106	711/158
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6539318 B2	20030325	13	Streaming architecture for waveform processing	702/66	324/121R
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6513094 B1	20030128		ROM/DRAM data bus sharing with write buffer and read prefetch activity	711/103	710/107; 710/200;
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6480929 B1	20021112		Pseudo-concurrency between a volatile memory and a non-volatile memory on a	711/105	365/185.08; 710/313;
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6473701 B1	20021029		Alternate triggering in digital oscilloscopes	702/67	324/121R; 702/66
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6442096 B2	20020827		Fast accessing of a memory device	365/230.01	365/230.06; 365/236
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6356269 B1	20020312		Image processing device and system using the same	345/505	345/520; 345/531;
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6307588 B1	20011023		Method and apparatus for address expansion in a parallel image	348/222.1	345/505; 345/547;
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6300963 B1	20011009		Single-frame display memory for spatial light modulator	345/547	345/574
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6209412 B1	20011002		Apparatus for controlling refresh of a	711/106	711/158

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6298413 B1	20011002		Apparatus for controlling refresh of a multibank memory device	711/106	711/168; 711/5
13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6259646 B1	20010710		Fast accessing of a memory device	365/230.01	365/230.08; 365/236
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6157751 A	20001205		Method and apparatus for interleaving a parallel image processing memory	382/304	708/233
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6026464 A	20000215		Memory control system and method utilizing distributed memory controllers	711/5	710/241; 710/242;
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5761713 A	19980602		Address aggregation system and method for increasing throughput to a	711/127	711/126; 712/23;
17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5659688 A	19970819		Technique and circuit for providing two or more processors with time	710/113	
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5596740 A	19970121		Interleaved memory conflict resolution with accesses of variable bank widths	711/157	365/189.02; 365/230.02;
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5587726 A	19961224		Method and apparatus for increasing the speed of operation of a double buffered	345/539	345/540
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5471588 A	19951128		Technique and circuit for providing two or more processors with time	710/113	710/241
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5396597 A	19950307		System for transferring data between processors via dual buffers within system	345/541	345/537
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5361343 A	19941101		Microprocessor system including first and second nonvolatile memory arrays	365/185.11	365/185.04; 365/185.22;
23			US 5210952 A	10020511		Memory control system for controlling a	707/1	711/112

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17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5659688 A	19970819		Technique and circuit for providing two or more processors with time	710/113	
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5596740 A	19970121		Interleaved memory conflict resolution with accesses of variable bank widths	711/157	365/189.02; 365/230.02;
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5587726 A	19961224		Method and apparatus for increasing the speed of operation of a double buffered	345/539	345/540
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5471588 A	19951128		Technique and circuit for providing two or more processors with time	710/113	710/241
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5396597 A	19950307		System for transferring data between processors via dual buffers within system	345/541	345/537
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5361343 A	19941101		Microprocessor system including first and second nonvolatile memory arrays	365/185.11	365/185.04; 365/185.22;
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5210852 A	19930511		Memory control system for controlling a first and second processing means to	707/1	711/112; 711/147
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4980845 A	19901225		Digital engine analyzer	701/33	701/102; 701/115
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4901009 A	19900213		Method and device for the automatic recording of signal curves	324/121R	702/66
26	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4818932 A	19890404	19	Concurrent memory access system	324/121R	345/440.1
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	EP 261751 A	19880330		Multi-processor concurrent memory access system e.g. for oscilloscope -		

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6564160 B2	20030513	18	Random sampling with phase measurement	702/106	331/172; 331/44;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6539318 B2	20030325	13	Streaming architecture for waveform processing	702/66	324/121R
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6128553 A	20001003		Menu control knob	701/3	340/980; 345/184;
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5742281 A	19980421		Image control device	345/501	
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5724069 A	19980303		Special purpose terminal for interactive user interface	345/172	345/173
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5724032 A	19980303		Method and apparatus for compressing and displaying digital data, particularly	341/50	345/440.1; 600/300;
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5684507 A	19971104		Method of displaying continuously acquired data on a fixed length display	345/440.1	345/660
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5610826 A	19970311		Analog signal monitor circuit and method	702/117	
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5594673 A	19970114		Method and apparatus for displaying menu options for selection with a	708/145	708/130
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5532714 A	19960702		Method and apparatus for combining video images on a pixel basis	345/634	348/591
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5440676 A	19950808		Raster scan waveform display rasterizer with pixel intensity gradation	345/443	345/611
40			US 5221420 A	19940614		Operator interface for an electronic	345/440.1	

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5321420 A	19940614		Operator interface for an electronic measurement system	345/440.1	
13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5309556 A	19940503		Method for using interactive computer graphics to control electronic instruments	345/771	315/383; 345/970
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5245323 A	19930914		Display device	345/13	345/20; 345/545
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5111191 A	19920505		Method and apparatus for waveform digitization	345/440.1	324/121R; 327/91;
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5050116 A	19910917		Keyboard for data processing equipment	708/145	
17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5025411 A	19910618		Method which provides debounced inputs from a touch screen panel by	345/175	345/173; 345/440.1
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5020012 A	19910528		Method and apparatus for matching menu labels with keys	708/144	
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4980845 A	19901225		Digital engine analyzer	701/33	701/102; 701/115
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4977514 A	19901211		Overlaid digital waveforms display	702/67	324/121R; 345/440.1
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4951229 A	19900821		Apparatus and method for managing multiple images in a graphic display	345/533	345/545; 345/634
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4935888 A	19900619		Computer keyboard template system	361/680	235/145R; 708/140
23			US 4945652 A	19900704		Generic equation solver interface for	709/144	709/168

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4980845 A	19901225		Digital engine analyzer	701/33	701/102; 701/115
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4977514 A	19901211		Overlaid digital waveforms display	702/67	324/121R; 345/440.1
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4951229 A	19900821		Apparatus and method for managing multiple images in a graphic display	345/533	345/545; 345/634
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4935888 A	19900619		Computer keyboard template system	361/680	235/145R; 708/140
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4845652 A	19890704		Generic equation solver interface for solving mathematical equations	708/144	708/168; 708/446
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4829293 A	19890509		Method and apparatus for achieving variable and infinite persistence	345/440.1	324/121R
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4818932 A	19890404	19	Concurrent memory access system	324/121R	345/440.1
26	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4799165 A	19890117		Level detecting waveform sampling system	702/66	324/76.15; 324/76.24;
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4634970 A	19870106	17	Digital waveform processing oscilloscope with distributed data	324/121R	324/102; 324/113;
28	<input checked="" type="checkbox"/>	<input type="checkbox"/>	SU 1758700 A	19920830		HV fuse in hermetic body - has two supplementary current-conducting films		
29	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4634970 A	19870106		Raster-display digital waveform processing oscilloscope - has signals		

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